

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	53888	power near5 (processor microprocessor cpu mpu)	US-PGPUB; USPAT	2005/02/17 15:04		
2	BRS	L2	31097	(stop\$4 sleep) same clock	US-PGPUB; USPAT	2005/02/17 15:05		
3	BRS	L3	10464	(low\$2 near5 power) same clock	US-PGPUB; USPAT	2005/02/17 15:06		
4	BRS	L4	1212	1 same 2	US-PGPUB; USPAT	2005/02/17 15:06		
5	BRS	L5	1289	1 same 3	US-PGPUB; USPAT	2005/02/17 15:06		
6	BRS	L6	1931	4 or 5	US-PGPUB; USPAT	2005/02/17 15:06		
7	BRS	L7	434381	(input\$4 receiv\$3) near3 data	US-PGPUB; USPAT	2005/02/17 15:08		
8	BRS	L8	1082	6 and 7	US-PGPUB; USPAT	2005/02/17 15:11		
9	BRS	L9	40902	(start\$3 wak\$3 activat\$3) near3 (processor microprocessor mpu cpu)	US-PGPUB; USPAT	2005/02/17 15:12		
10	BRS	L10	567	8 and 9	US-PGPUB; USPAT	2005/02/17 15:12		
11	BRS	L11	861	9 same independent\$2	US-PGPUB; USPAT	2005/02/17 15:13		
12	BRS	L12	15	10 and 11	US-PGPUB; USPAT	2005/02/17 15:24		
13	BRS	L13	2350	713/300-320.ccls.	US-PGPUB; USPAT	2005/02/17 15:25		
14	BRS	L14	1418	713/322-324.ccls.	US-PGPUB; USPAT	2005/02/17 15:26		
15	BRS	L15	18	11 and 14	US-PGPUB; USPAT	2005/02/17 15:26		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
16	BRS	L16	12	15 not 12	US-PGPUB; USPAT	2005/02/17 15:48		
17	BRS	L17	20	6 and 11	US-PGPUB; USPAT	2005/02/17 15:48		
18	BRS	L18	5	17 not 12	US-PGPUB; USPAT	2005/02/17 15:48		